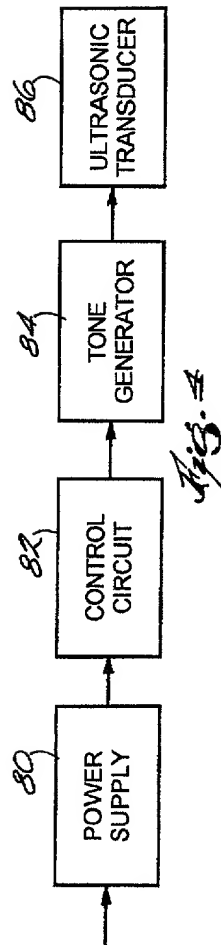
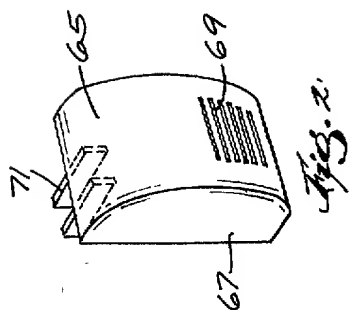
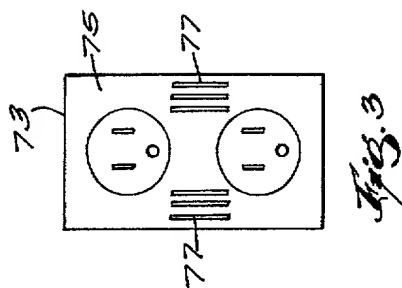


Fig. 1



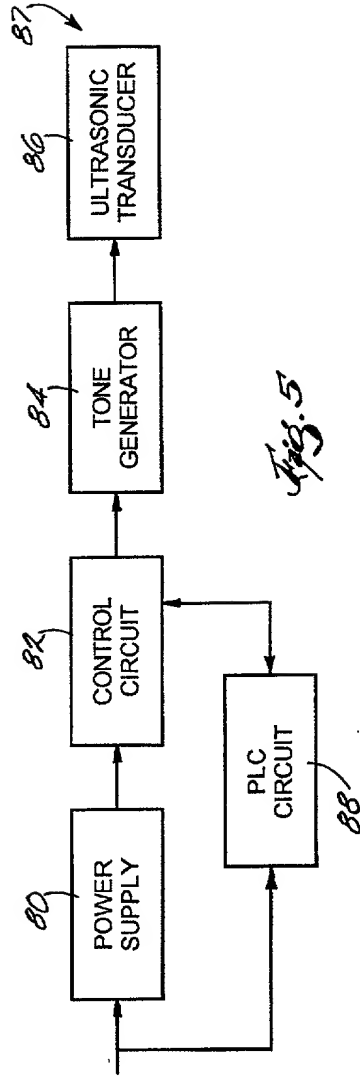


FIG. 6 is a block diagram of the system.

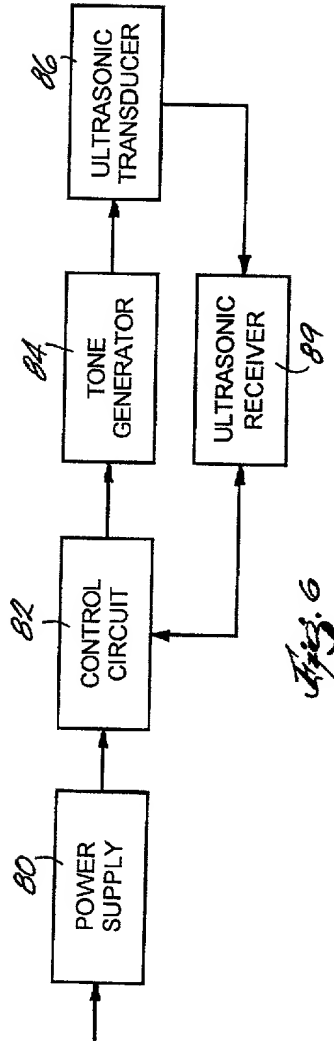
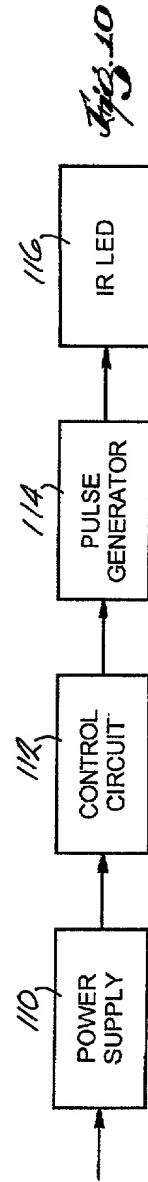
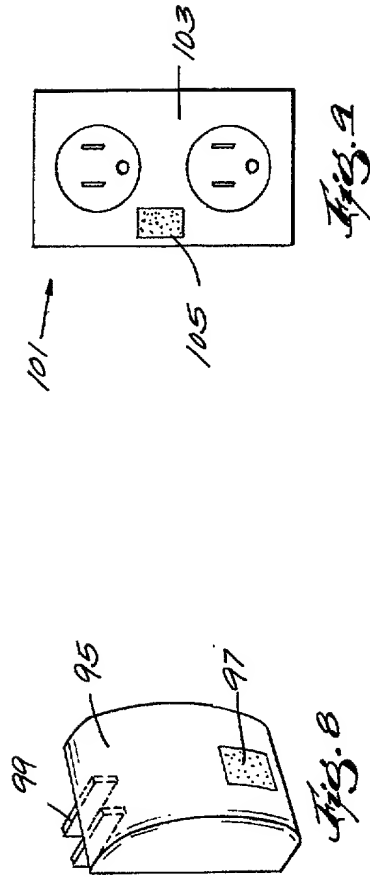


Fig. 6





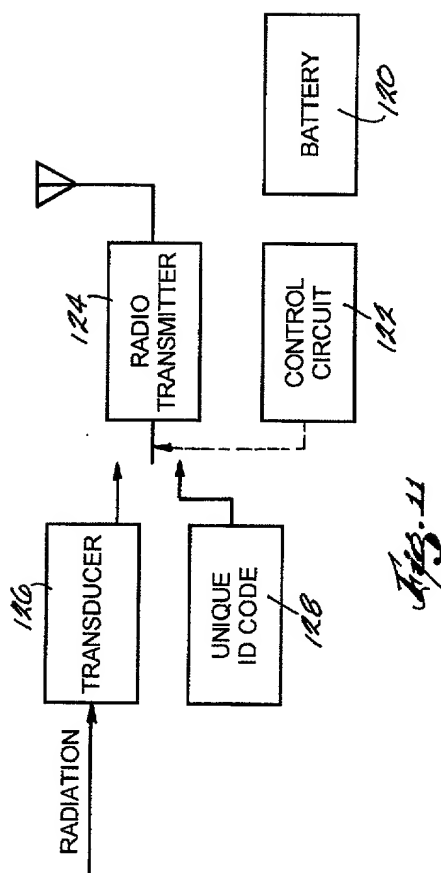
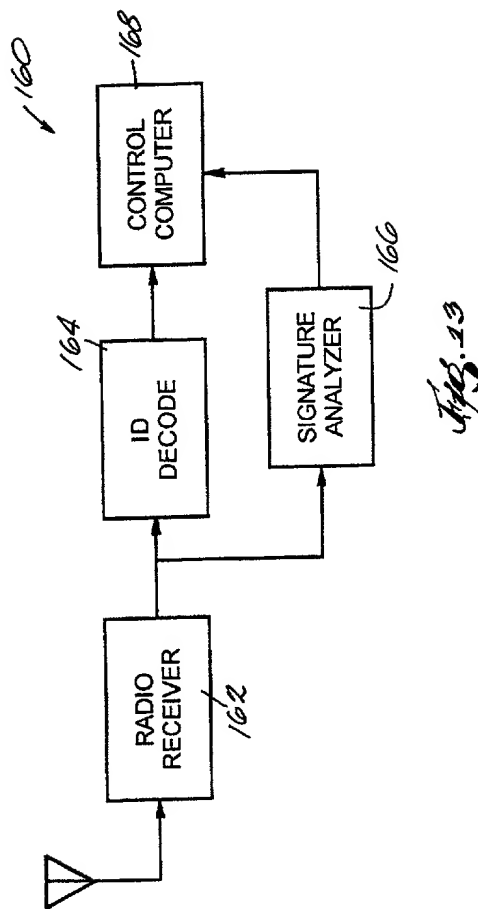


Fig. 12.





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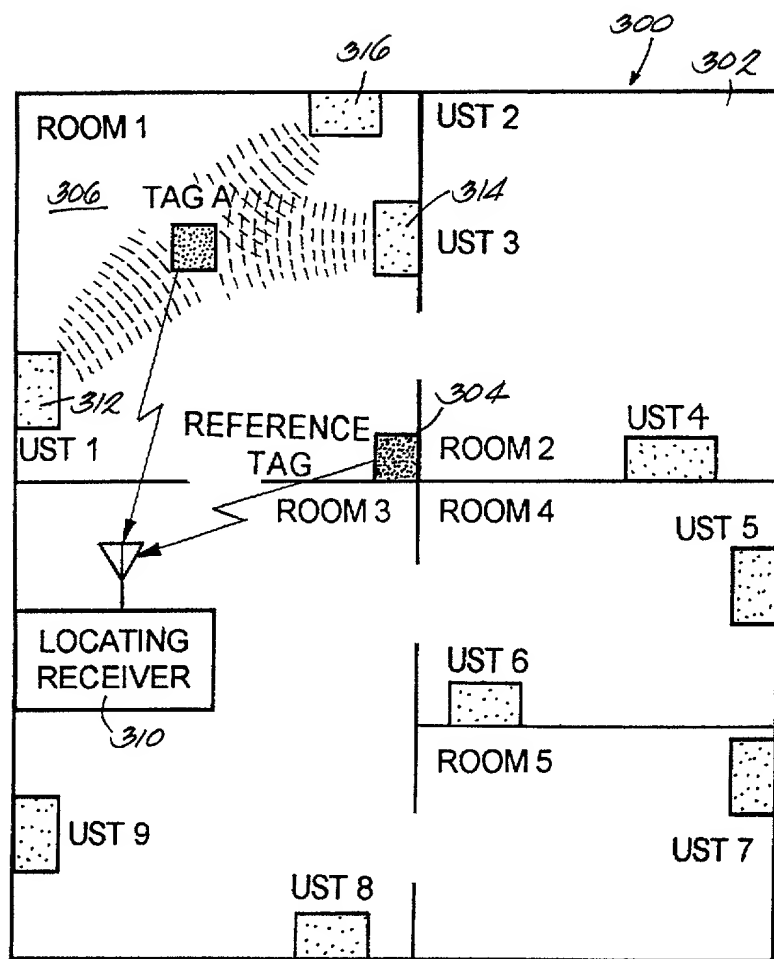


Fig. 16

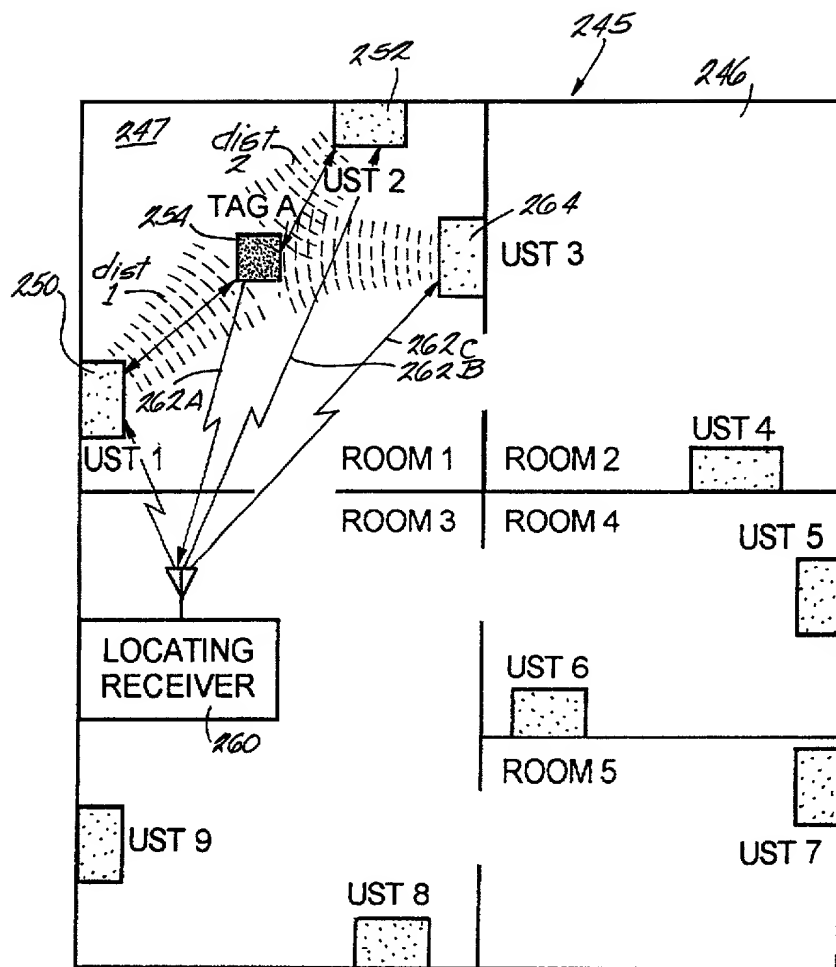


Fig. 15

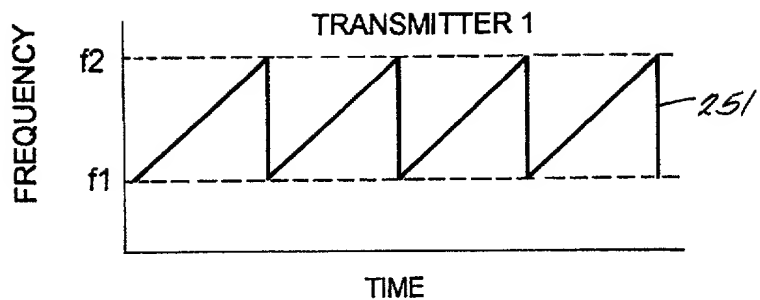


Fig. 17

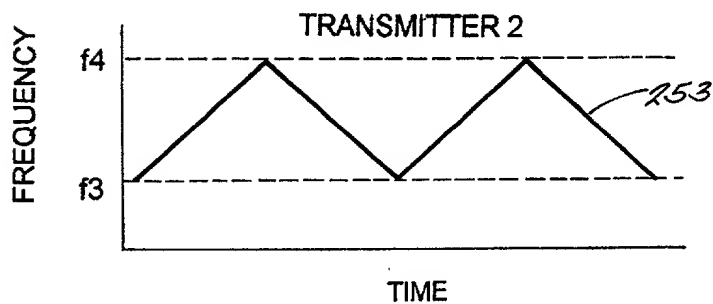
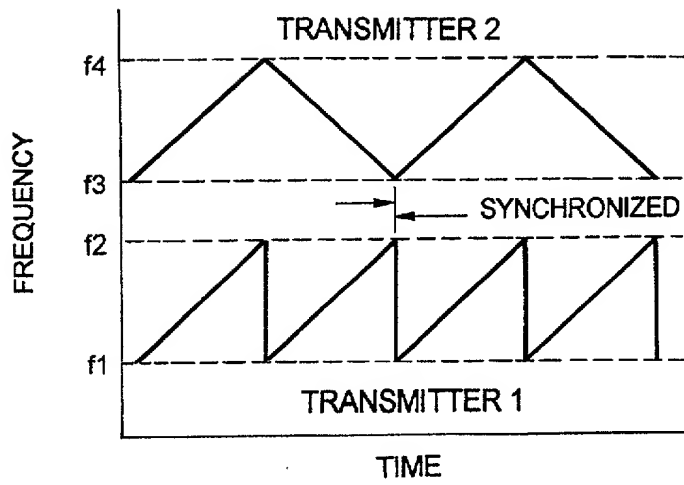
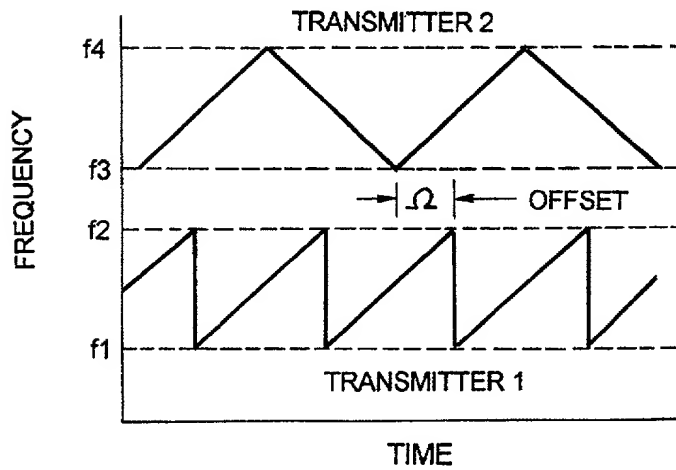


Fig. 18



*Fig. 19*



*Fig. 20*

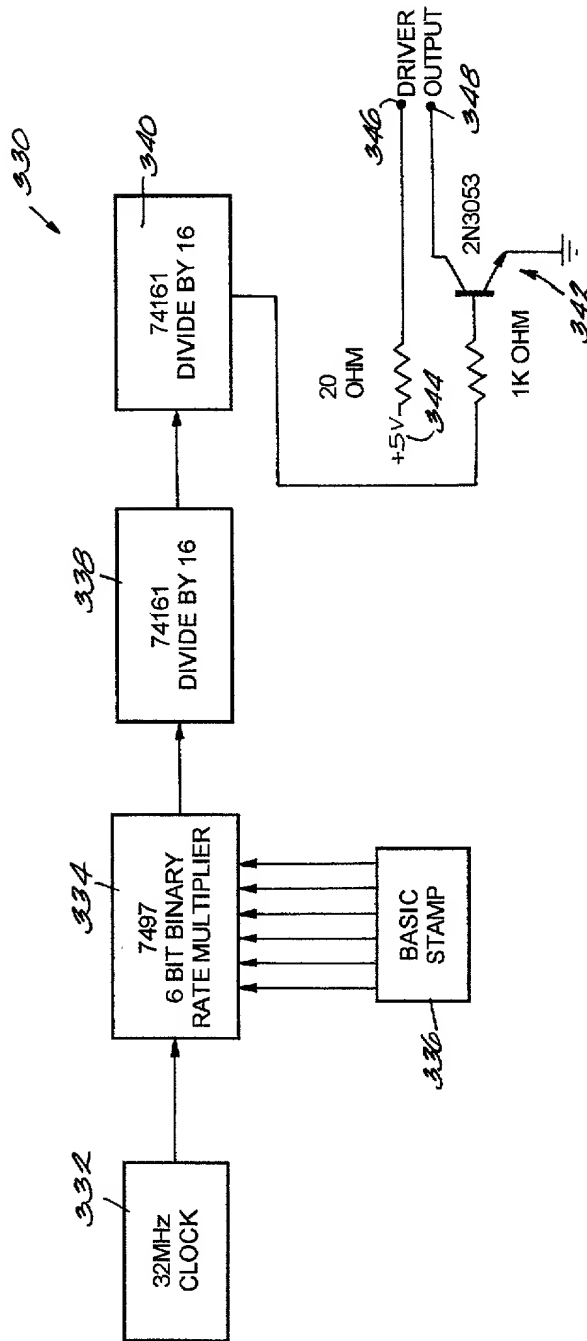


Fig. 21

PROGRAMMABLE SQUARE WAVE DRIVER  
2 KHz TO 123 KHz IN 1.95 KHz STEPS

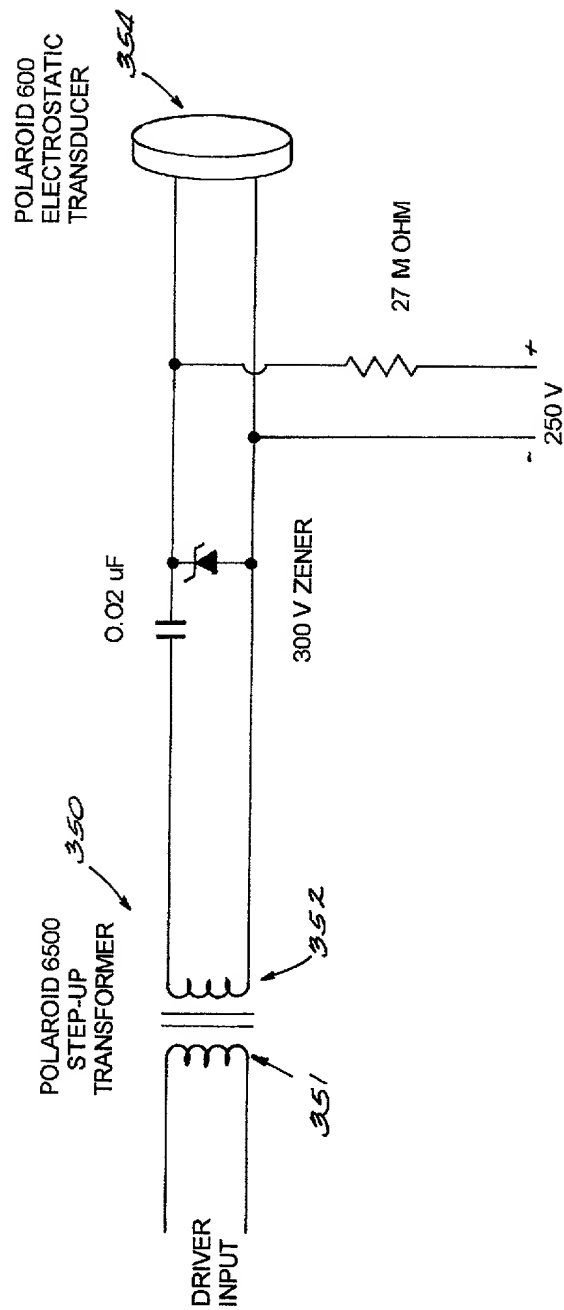


Fig. 10

TRANSDUCER INTERFACE CIRCUIT